

<b>Form PTO-1449 (Modified)</b>				<b>Atty Docket No.:</b> 004073.P015		<b>Serial No.:</b> 09/823,802	
<b>List of Patents and Publications Statement</b> (Use several sheets if necessary)				<b>Applicants:</b> Gautam Dewan et al. <b>Filing Date:</b> March 30, 2001			

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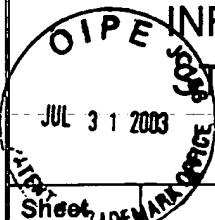
REFERENCE DESIGNATION				U.S. PATENT DOCUMENTS			
Exam. Initial	Date	Document Number	Name	Class	Sub-Class	Filing Date	
✓	12/5/00	6,157,955	Narad et al.	709	228	6/15/98	
	1/9/01	6,172,980	Flanders et al.	370	401	9/11/97	
	12/5/00	6,157,641	Wilford	370	389	8/22/97	
	12/12/00	6,160,811	Partridge et al.	370	401	9/12/97	
✓	9/8/98	5,805,808	Hasani et al.	395	200.2	4/9/97	

FOREIGN PATENT DOCUMENTS							
No.	Document No.	Date	Country	Name	Class	Sub-Class	Translation


OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)		

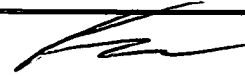
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Substitute for Form 1449/PTO			<b>Complete if Known</b>	
			Application Number	09/823,802
			Filing Date	March 30, 2001
			First Named Inventor:	Gautam Dewan
			Art Unit	2857
			Examiner Name	Not Assigned.
Sheet 1	of 4	Attorney Docket Number	4073P015	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)				
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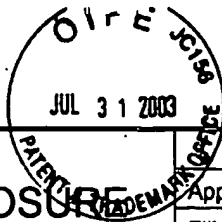
FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup>	Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
		WO 01/16698	A2	03/08/2001	Wolrich et al.		

Examiner Signature		Date Considered	4/7
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SENT FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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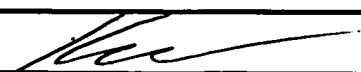
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Sheet 2 of 4

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		PCT Notification of Transmittal of International Search Report or the Declaration for PCT Counterpart Application No. PCT/US02/09849 Containing International Search Report (November 11, 2002).	
		X. Jin et al., "Implementing Click IP Router Kernel on VLIW Architectures," XP-002208124, CS252 Class Project, University of California at Berkeley, pp. 1-5, May 2000.	
		Project Page, XP-002208125, CS252 - Spring 2000, University of California at Berkeley, pp. 1-2, March 2000.	
		"The Challenge for Next Generation Network Processors," XP-002208126, Agere, Inc., pp. 1-7, September 10, 1999.	
		"PayloadPlus™ Functional Programming Language," XP-002208127, Preliminary Product Brief, Microelectronics Group, Lucent Technologies, Bell Labs Innovations, pp. 1-6, April 2000.	
		"PayloadPlus™ Agere System Interface," XP-002208128, Preliminary Product Brief, Microelectronics Group, Lucent Technologies, Bell Labs Innovations, pp. 1-6, April 2000.	
		N. Shah, "Understanding Network Processors," XP-002208129, Version 1.0, <a href="mailto:nirai@eecs.berkeley.edu">nirai@eecs.berkeley.edu</a> , pp. 1-89, September 4, 2001.	
		"PayloadPlus™ Routing Switch Processor," XP-002208130, Product Brief, Agere Systems, pp. 1-5, July 30, 2001.	
		W.H. Mangione-Smith, G. Memik, "Network Processor Technologies," XP-002208131A, Network Processor Technologies Tutorial, Micro 34, <a href="mailto:billms@ucla.edu">billms@ucla.edu</a> , <a href="mailto:memik@ee.ucla.edu">memik@ee.ucla.edu</a> , pp. 1-94, December 1, 2001.	
		"Parallel eXpress Forwarding in the Cisco 10000 Edge Service Router," XP-002208132, White Paper, Cisco Systems, Inc., pp. 1-7, December 31, 2000.	
		E. Kohler et al., "The Click Modular Router," XP-002208133, Laboratory for Computer Science, MIT, ACM Transactions on Computer Systems, Vol. 18, No. 3, pp. 263-296, (August 2000).	

Examiner Signature		Date Considered	4/7/05
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
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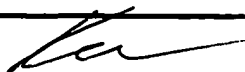
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Art Unit	2857
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Attorney Docket Number	4073P015

Sheet 3 of 4

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Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		F. Kurfeß*, "Potentiality of Parallelism in Logic," TR-91-055, Preprint from: Wrightson, G and Fronhofer, B.; Parallelization in Inference Systems, Springer Verlag (1991).	
		G. Swallow, "MPLS Advantages for Traffic Engineering," Cisco Systems, IEEE Communication Magazine, December 1999.	
		D. Wilson and R. V. Berry, "Amber Networks Unveils Industry's Most Advanced Optical Internet Edge Router," <a href="http://www.ambernetworks.com/press_release.html?id=2">http://www.ambernetworks.com/press_release.html?id=2</a> (June 6, 2000).	
		"The Technology of IP Routers in the New Public Network," White Paper, Ericsson IP Infrastructure, pp. 1-21, January 2000.	
		"Unleash the Power, Building Multi-Service IP Networks With ATM Cores," The ATM Forum, pp.1-12, March 1999.	
		"Encapsulation," <a href="http://whatis.techtarget.com/WhatIs_Search_Resu.../282033..00html?query=encapsulatio">http://whatis.techtarget.com/WhatIs_Search_Resu.../282033..00html?query=encapsulatio</a> (Oct. 14, 1999).	
		"Point-to-Point Protocol," <a href="http://whatis.techtarget.com/WhatIs_Definition_Page/0,4152,214311,00.html">http://whatis.techtarget.com/WhatIs_Definition_Page/0,4152,214311,00.html</a> (November 3, 2000).	
		"User Datagram Protocol," <a href="http://whatis.techtarget.com/WhatIs_Search_Results_Exact/1,282033..00.html?query=UDP">http://whatis.techtarget.com/WhatIs_Search_Results_Exact/1,282033..00.html?query=UDP</a> (October 10, 2000).	
		"Internet Protocol," <a href="http://whatis.techtarget.com/whatis.techtarget.com/WhatIs_Definition_Page/0,4152,214031,00.html">http://whatis.techtarget.com/whatis.techtarget.com/WhatIs_Definition_Page/0,4152,214031,00.html</a> (October 5, 2000).	
		P. Wayner, "Will VLIW mean 'very long investment window' for Hewlett-Packard and Intel?," Core Technologies, VLIW Questions, <a href="http://www.byte.com/art/9411/sec12/art1.htm">http://www.byte.com/art/9411/sec12/art1.htm</a> (November 1994).	
		"VLIW at IBM Research," IBM Research, <a href="http://www.research.ibm.com/vliw/">http://www.research.ibm.com/vliw/</a> (1995).	

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		Baron, Robert J. et al., "Computer Architecture Case Studies," pp. xii-xiv, 25-34, 131-145 (Addison-Wesley Pub. Co., Inc. 1992).	
		Patterson, David A., et al., "Computer Architecture A Quantitative Approach," pp. xi-xv, 208-214, 322-325, 408-427, 571-594 (Morgan Kaufmann Publishers, Inc. 1990).	

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